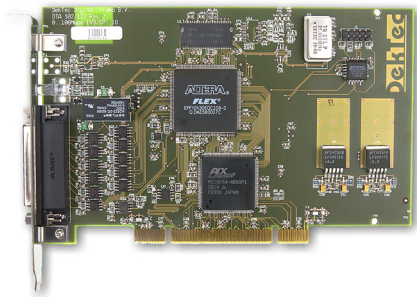


DVB-SPI(LVDS) Input Adapter for PCI Bus

DekTec DTA-122



- 0 to 108 Mbit/s
- 8MBytes buffer
- Support for external signal adapters

KEY FEATURES

- High speed transport stream input, compliant to DVB-SPI (synchronous parallel interface) as defined in DVB document A010 rev 1 and EN50083
- Special support for external signal adapters from e.g. ECL or TTL
- Input bit rate 0 to 108 Mbit/s
- LED indicator shows synchronization status
- 8Mbytes on board buffer
- Optional FEC stripping
- Support for 188 byte and 204 byte packets, or arbitrary packet size in raw mode
- On-board PCI Bus master for off loading host processor
- DMA burst mode transfers for optimal usage of PCI Bus
- PCI rev 2.2, 32 bit, 33 MHz
- Comes with free:
 - Windows 2000/XP/2003 & Linux device driver and software development kit for developing custom applications
 - Example source code for TS recorder
 - DtGrabber: Windows recorder program

APPLICATIONS

- General purpose adapter in PC based applications for the reception of an MPEG2 transport stream on DVB-SPI
- DVB-SPI transport stream recording
- With DTC-320 StreamXpert software: Versatile MPEG2 transport stream analysis and monitoring tool
- With DTC-330 DtTV software: Decoding of one television service from a transport stream, or decoding of multiple services for mosaic creation

KEY ATTRIBUTES

Parameter	Value
Physical layer	DVB-SPI (LVDS)
DVB-SPI connector	25-pin sub-D
DVB-SPI clock	0 to 13.5 MHz
Input bit rate	0 to 108 Mbit/s
Packet size in bytes	188 or 204*
Target adapter power	5 V, 2 A

RELATED PRODUCTS

Type	Description
DTA-102	DVB-SPI Output Adapter for PCI Bus
DTC-320	StreamXpert TS Analyzer Software
DTC-330	DtTV TS Television Software

ORDERING INFORMATION

Type	Description
DTA-122-SP DVB-SPI	Input adapter for PCI bus with DtTV and DtGrabber+.
DTA-122-SX	DTA-122-DP with StreamXpert.